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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/956,001 09/20/2001 PAT 6,611,015

** FOREIGN APPLICATIONS *****

JAPAN 2000-284710 09/20/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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| Foreign Priority claimed | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR | SHEETS | TOTAL | INDEPENDENT |
| 35 USC 119 (a-d) conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | COUNTRY | DRAWING | CLAIMS | CLAIMS |
| Verified and Acknowledged | | JAPAN | 25 | 2 | 2 |
| | Initials | | | | |

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TITLE

Semiconductor memory device and method of fabricating the same

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